

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

4. (Currently Amended) A data processing device, comprising:

an array of data processing units, the data processing units being connected to at least one of a power supply line and a clock line; and

an enabling/disabling ~~element~~ device ~~configured~~ adapted to at least one of: i) enable or disable power supply to a number of the data processing units, and ii) block full clock speed for the number of data processing units;

wherein the number is less than all of the data processing units; and

wherein the enabling/disabling device is configured to be data driven.

5. (Currently Amended) The data processing device according to claim 4, wherein the enabling/disabling ~~element~~ device is adapted to provide a clock to a number of the data processing units which is equal to 0.

6. (Previously Presented) The data processing device according to claim 4, wherein the enabling/disabling device is adapted to be handshake-driven.

7. (Previously Presented) The data processing device according to claim 4, wherein each of the data processing units is a reconfigurable unit of a multi-dimensional array.

8. (Currently Amended) The data processing device according to claim 4, wherein the enabling/disabling ~~element~~ device is ~~configured~~ adapted to selectively enable or disable the power supply to the number of data processing units.

9. (Currently Amended) The data processing device according to claim 4, wherein the enabling/disabling ~~element~~ device is ~~configured~~ adapted to selectively block full clock speed for the number of data processing units.

10. (New) The data processing device according to claim 4, wherein the enabling/disabling device is adapted to be data availability driven.

11. (New) The data processing device according to claim 4, wherein the number of data processing units includes only a single one of the data processing units in the array.

12. (New) A data processing device, comprising:

an array of data processing units, the data processing units being connected to at least one of a power supply line and a clock line; and

an enabling/disabling device adapted to, in response to an availability of data for at least one respective one of the data processing units, at least one of: i) selectively enable or disable power supply to the at least one respective one of the data processing units, and ii) selectively block full clock speed for the at least one respective one of the data processing units;

wherein the at least one respective one of the data processing units includes less than all of the data processing units in the array.

13. (New) The data processing device according to claim 12, wherein the enabling/disabling device enables the power supply to the respective one of the data processing units only when data is available for the respective one of the data processing units.

14. (New) The data processing device according to claim 12, wherein the enabling/disabling device is adapted to make a clock signal available to the respective one of the data processing units only when an operand is ready for the respective one of the data processing units.

15. (New) The data processing device according to claim 12, wherein the enabling/disabling device is associated with only a single one of the data processing units.

16. (New) The data processing device according to claim 12, wherein the at least one respective one of the data processing units includes only a single one of the data processing units in the array.

17. (New) A data processing device, comprising:

an array of data processing units, the data processing units being connected to at least one of a power supply line and a clock line; and

an enabling/disabling device adapted to make a clock signal available to at least one respective one of the data processing units when an operand is ready for the at least one respective one of the data processing units; and

wherein the at least one respective one of the data processing units includes less than all of the data processing units in the array.

18. (New) The data processing element according to claim 17, wherein the enabling/disabling device is associated with only a single one of the data processing units.

19. (New) The data processing device according to claim 17, wherein the at least one respective one of the data processing units includes only a single one of the data processing units in the array.